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OKAMURA YOSHIMI****(54) DISC ACCESS DEVICE****(57)Abstract:**

**PURPOSE:** To attain high-speed disc access of the titled device by allowing a host computer to access directly data in a disc buffer memory via a window to decrease the access time of the data in the memory.

**CONSTITUTION:** The host computer 4 informs the head address and size of a window area to a window control circuit 12 and sets the window in the own address space. Further, a window address conversion register 13 decides that the window area corresponds which area in an actual disc buffer memory 2. The computer 4 gives a window opening and closing command to the circuit 12 to actuate the window function. Further, the command from the computer 4 is fed to an internal control circuit 9. Then the data in the memory 2 is accessed directly by the computer 4 via the window to reduce the access time in the memory 2 thereby speeding up the disc access of the disc access device.

